REMARKS

Claims 1-20 remain pending in the current Application. Applicants wish to thank the Examiner for pointing out allowable subject matter.

Rejection of claims 1, 7-10, 16, and 20 under 35 U.S.C. 102(b)

Applicants respectfully submit that claims 1, 7-10, 16, and 20 are patentable over US Patent No. 5,742,785 (hereinafter referred to as Stone). For example, with respect to claim 1, Applicants submit that Stone does not teach or suggest invalidating the reservation in response to receiving notice that an interrupt is pending in the data processing system. The Examiner cites that col. 10, lines 48-52, and col. 15, lines 34-36, teaches an interrupt invalidating the reservation, processing the interrupt and executing the conditional store. However, Applicants respectfully disagree. As described in reference to FIG. 4 and col. 10, lines 36-55, of Stone, a reservation is invalidated when a remote processor modifies or writes to a reserved shared variable. Therefore, as stated in col. 10, lines 48-52, nothing is updated when a Write-if-Reserved instruction is later executed because of the invalid reservation caused by the previous modification by the remote processor. However, there is no teaching or suggestion in col. 10, lines 36-55, of receiving notice that an interrupt is pending where the reservation is invalidated in response to receiving that notice. That is, a remote processor modifying a reserved shared variable does not teach or suggest an interrupt, as claimed in claim 1. Furthermore, none of the cited sections of Stone even discuss interrupts or how interrupts affect the LR, SC, and WR instructions. Therefore, for at least these reasons, Applicants submit that claim 1 is allowable over Stone. Claims 7-9 depend directly or indirectly from allowable claim 1 and are therefore also allowable for at least those reasons provided above with respect to claim 1.

With respect to claims 10 and 16, Stone does not teach or suggest a reservation register where, in response to the data processing system receiving an interrupt, the reservation is cancelled. That is, as described above with respect to claim 1, Stone does not even discuss interrupts. In the system of Stone, a reservation is invalidated when a remote processor modifies a reserved shared variable, but there is not teaching or suggestion in Stone of canceling a reservation in response to receiving an interrupt. Therefore, for at least these reasons,

Applicants submit that claims 10 and 16 are allowable over Stone. Claim 20 depends from claim 16, and is therefore also allowable for at least those reasons provided with respect to claim 16.

Conclusion

Although Applicants may disagree with statements made by the Examiner in reference to the claims and the cited references, Applicants are not discussing all these statements in the current Office Action, yet reserve the right to address them at a later time if necessary.

Applicants respectfully solicit allowance of the pending claims. Contact me if there are any issues regarding this communication or the current Application.

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079.

SEND CORRESPONDENCE TO:

Freescale Semiconductor, Inc. Law Department

Customer Number: 23125

Respectfully submitted,

Joanna G. Chiu Attorney of Record

Reg. No.: 43,629

Telephone: (512) 996-6839 Fax No.: (512) 996-6854